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PTO/SB/08a (08-03)

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		10657331
	Filing Date		2003-09-08
	First Named Inventor	William C. Moyer	
	Art Unit	2183	
	Examiner Name	Daniel H. Pan	
	Attorney Docket Number	SC13074TH	

U.S. PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
h	1	6986023	B2	2006-01-10	Paver et al.	
h	2	6950922	B2	2005-09-27	Chung et al.	
h	3	6292886	B1	2001-09-18	Makineni et al.	
h	4	6081880		2000-06-27	Sollars	
h	5	6052766		2000-04-18	Betker et al.	
h	6	5903769		1999-05-11	Arya	
h	7	5838984		1998-11-17	Nguyen et al.	
h	8	5799163		1998-08-25	Park et al.	

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W	9	5669013		1997-09-16	Watanabe et al.	
G	10	5530804		1996-06-25	Edgington et al.	
W	11	5475852		1995-12-12	Yoshida et al.	
W	12	5361354		1994-11-04	Grayzek	
W	13	5201056		1993-04-06	Daviel et al.	
W	14	4825361		1989-04-25	Omoda et al.	
W	15	4760545		1988-07-26	Inagami et al.	
W	16	4677547		1987-06-30	Omoda et al.	
W	17	6898691	B2	2005-05-24	Blomgren et al.	

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#### U.S. PATENT APPLICATION PUBLICATIONS

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<i>LP</i>	1	20050053012	A1	2005-03-10	Moyer	
<i>W</i>	2	20050055543	A1	2005-03-10	Moyer	
<i>W</i>	3	20030167460	A1	2003-09-04	Desai et al.	

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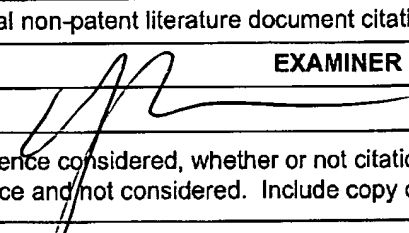
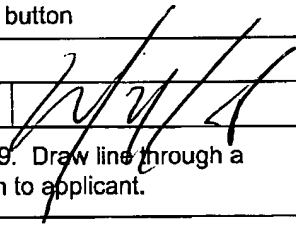
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	Art Unit	2183
	Examiner Name	Daniel H. Pan
	Attorney Docket Number	SC13074TH

6	1	TALLA, DEEPENDRA; "Architectural Techniques to Accelerate Multimedia Applications on General-Purpose Processors"; University of Texas Doctoral Dissertation: August, 2001; pp 94-125 (Chapters 6 and 7); University of Texas, Austin, Texas.	<input type="checkbox"/>
W	2	TALLA, DEEPU et al.; "Bottlenecks in Multimedia Processing with SIMD Style Extensions and Architectural Enhancements"; IEEE Transactions of Computers; August, 2003; pp 1015-1031; Vol. 62, No 8; IEEE	<input type="checkbox"/>
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	10/657,331
		Filing Date	September 8, 2003
		First Named Inventor	William C. Moyer et al.
		Group Art Unit	2183
		Examiner Name	Unknown
Sheet 1 of 1	Attorney Docket Number	SC13074TH	

U. S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
la	DA	US -6,598,221	B1	Pegatoquet et al.	07-22-2003	
la	DB	US -5,450,807		Kowalczyk et al.	09-12-1995	

FOREIGN PATENT DOCUMENTS								
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS					
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	DC	KAVI, Krishna M. et al. ; "A Formal Definition of Data Flow Graph Models"; IEEE; 1988			

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

1

of

2

**Complete if Known**

Application Number

Filing Date

**First Named Inventor**

Group Art Unit

Examiner Name

**William C. Moyer**

Attorney Docket Number

SC13074TH

## U. S. PATENT DOCUMENTS

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Sheet	2	of	2

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Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
u	AJ	WULF, WM. A., "Evaluation of the WM Architecture", ACM ; 7/92; pp 382-390.	
w	AK	Related application number 09/591,938 filed June 12, 2000	
h	AL	Related application filed concurrently herewith, attorney docket number SC13053TH	
h	AM	Related application filed concurrently herewith, attorney docket number SC13071TH	
e	AN	Related application filed concurrently herewith, attorney docket number SC13075TH	

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